

į)

FIG. 2

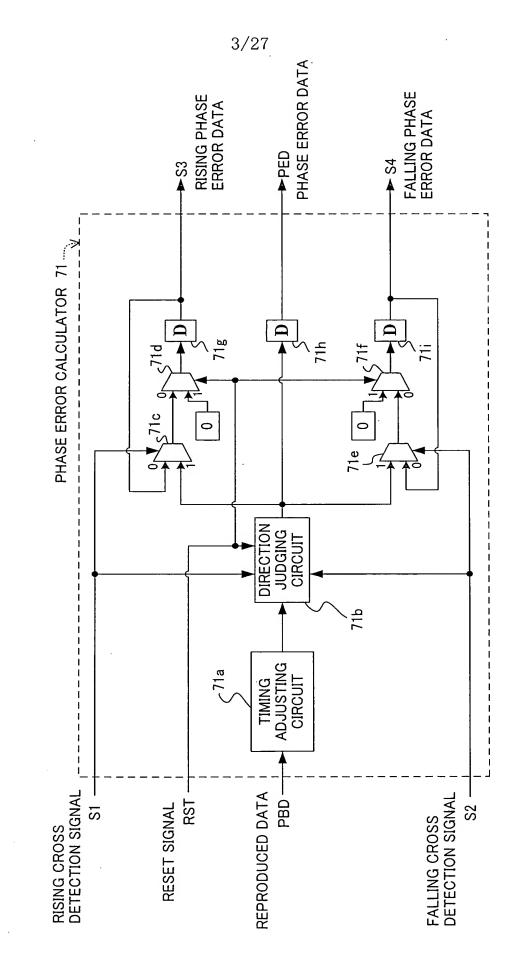


FIG. 3

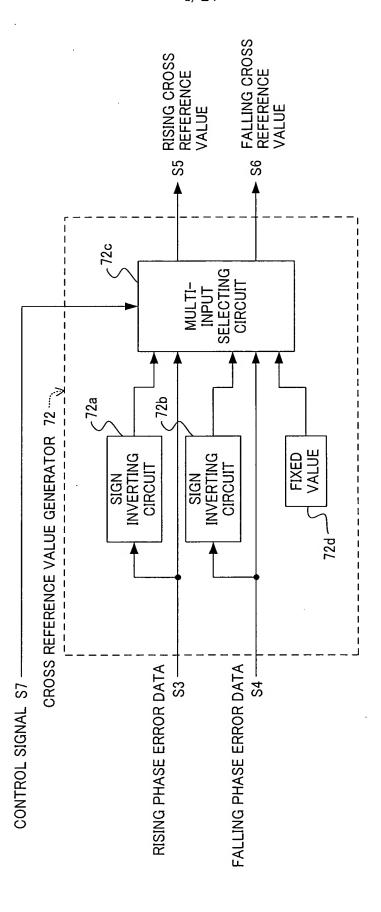
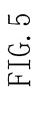


FIG. 4



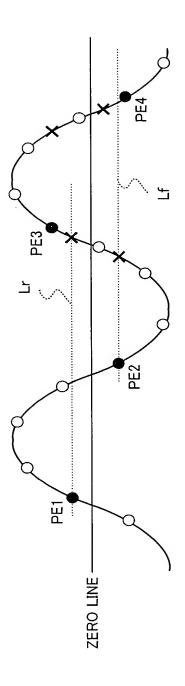
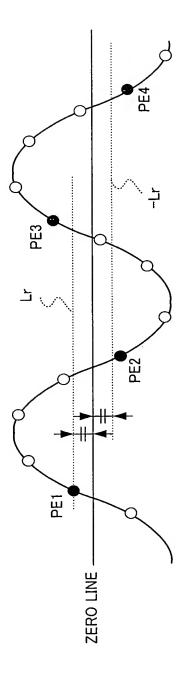


FIG. 6



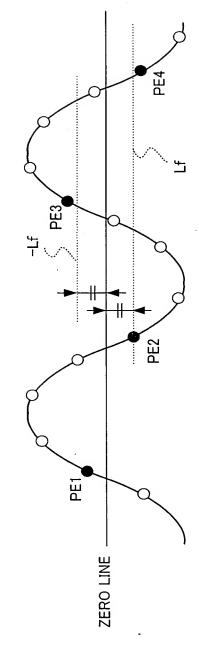
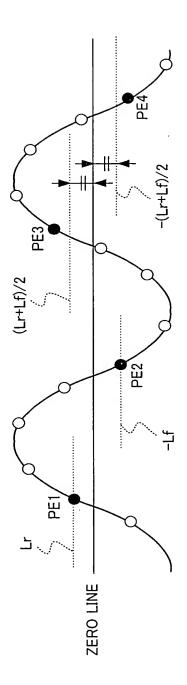


FIG. 7

FIG. 8



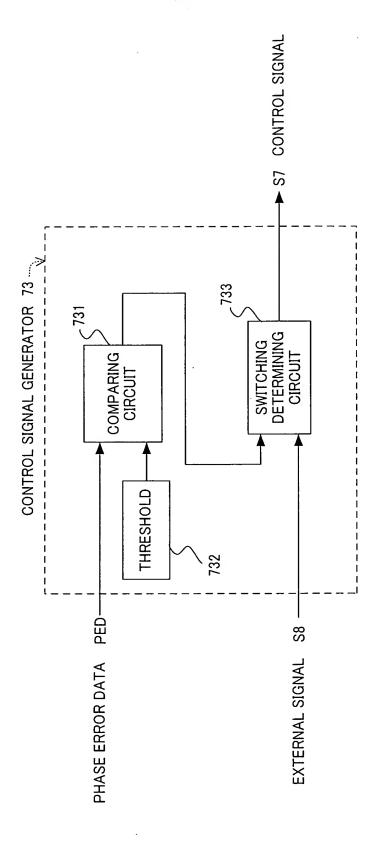


FIG. 9

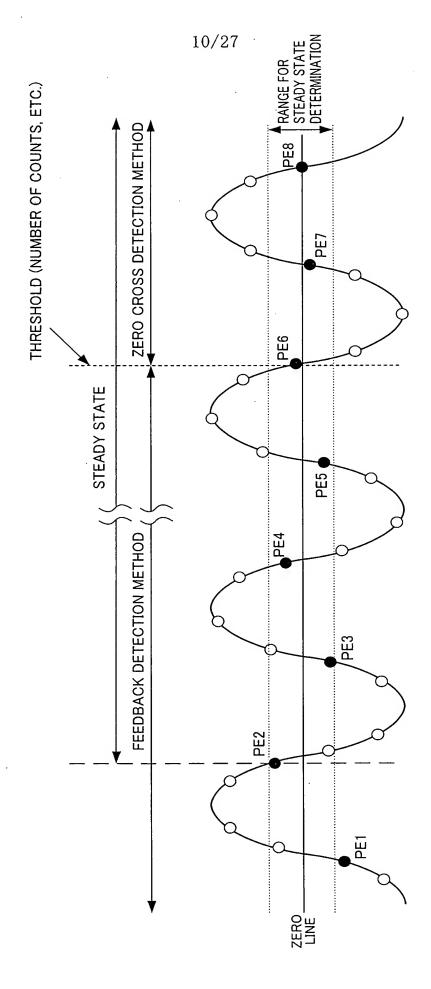
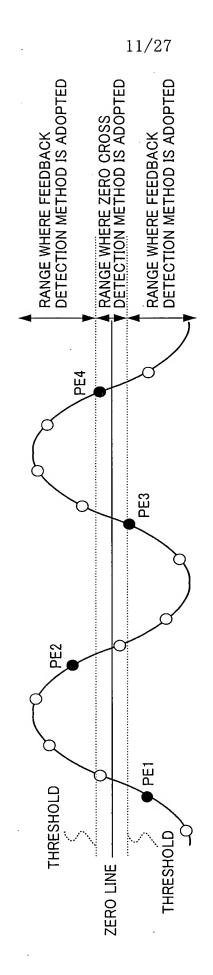
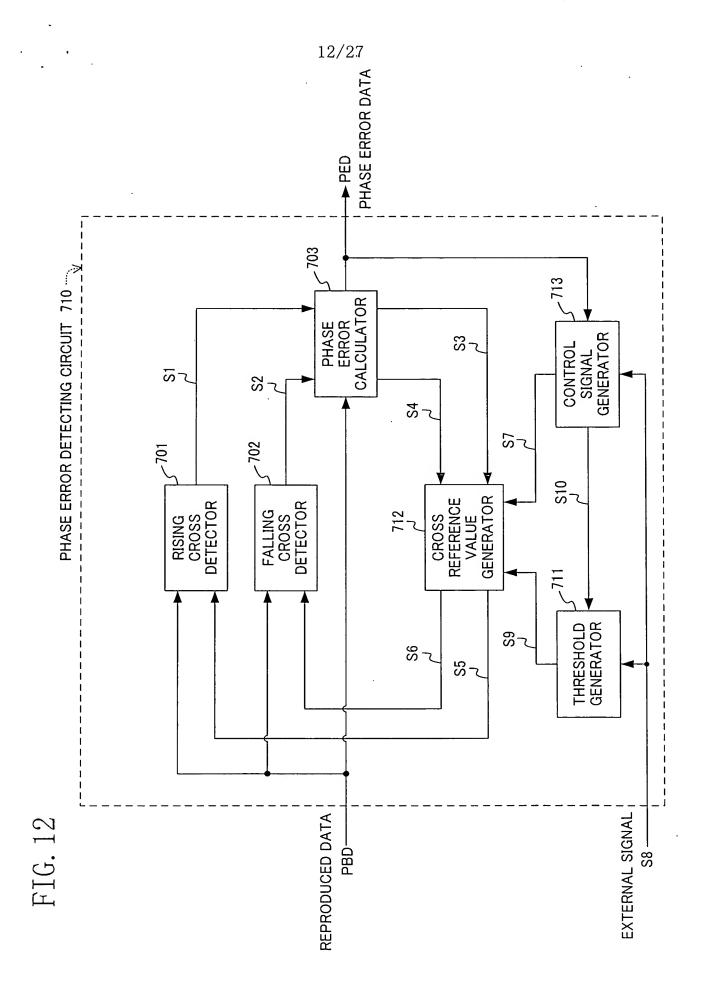


FIG. 10







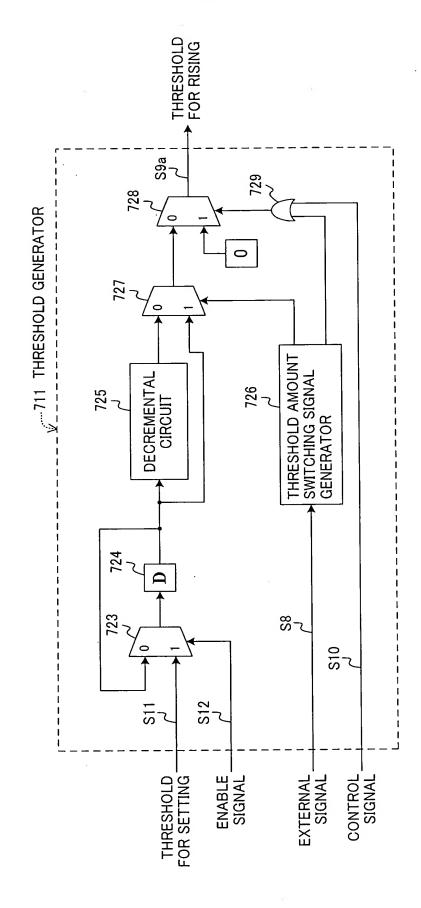


FIG. 13

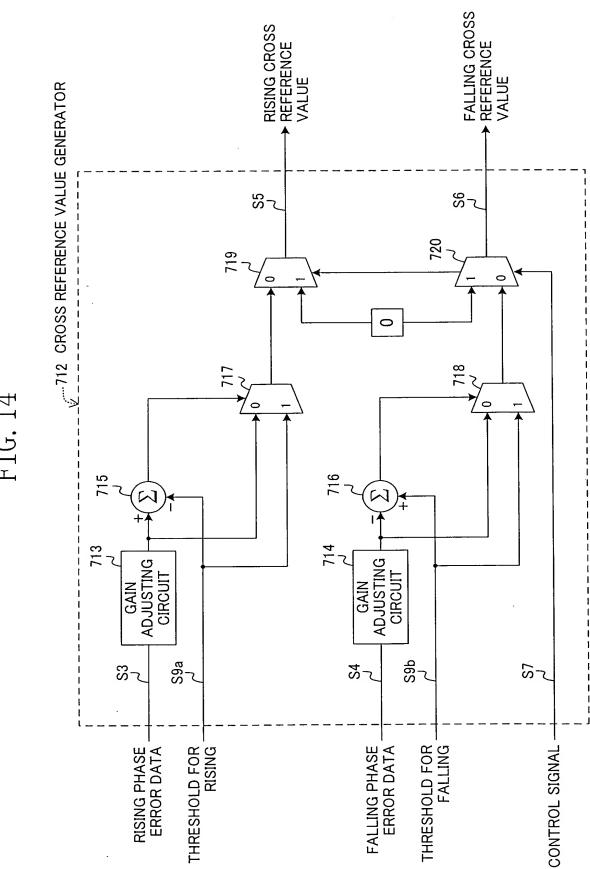


FIG. 14

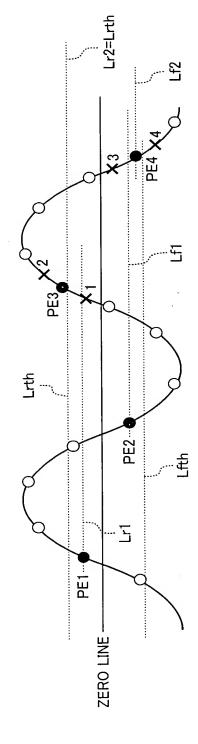
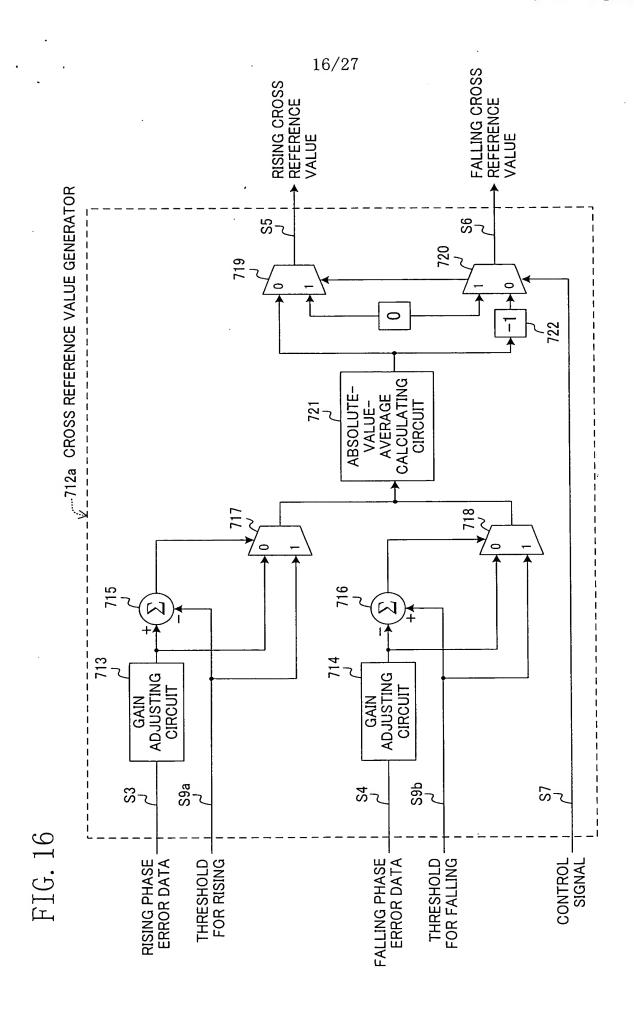


FIG. 15



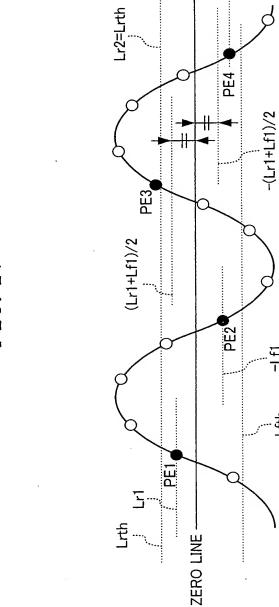
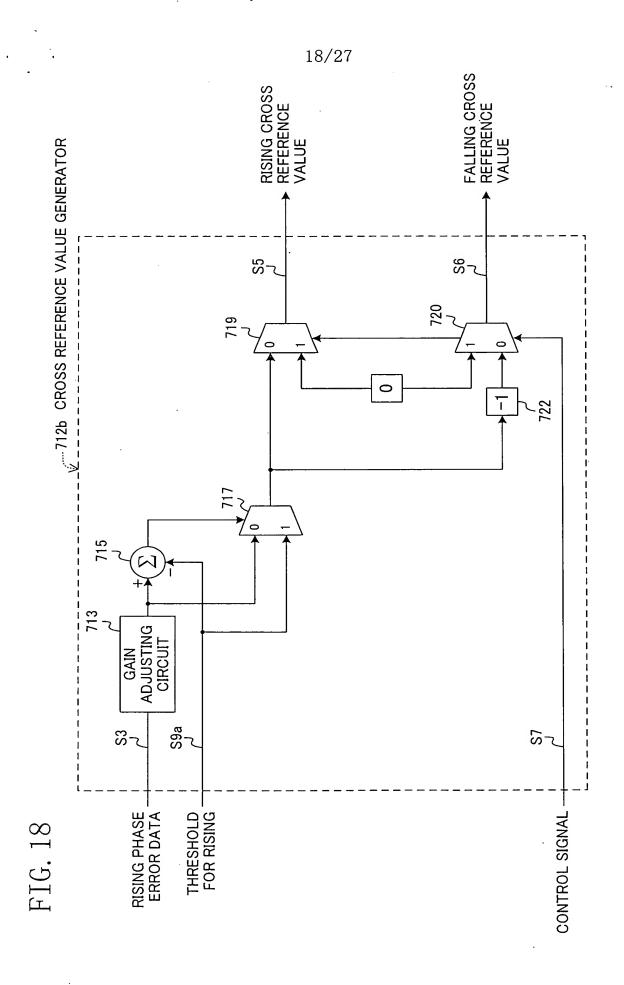
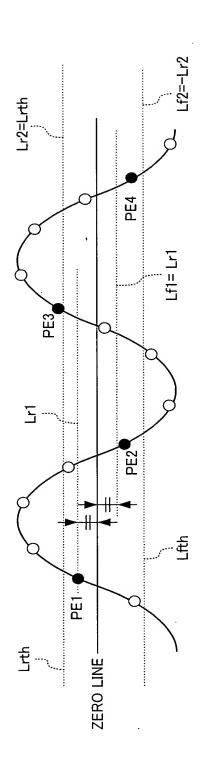


FIG. 17







Lr1=-Lf1

FIG. 20

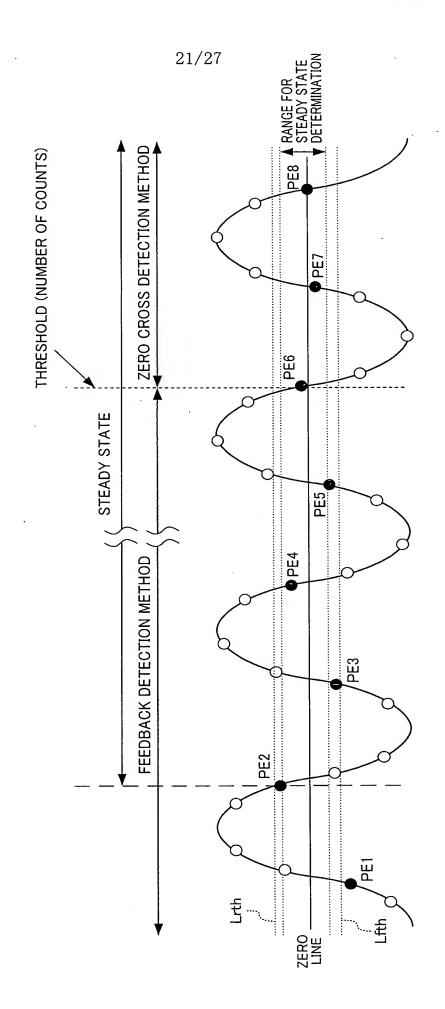


FIG. 21

FIG. 22

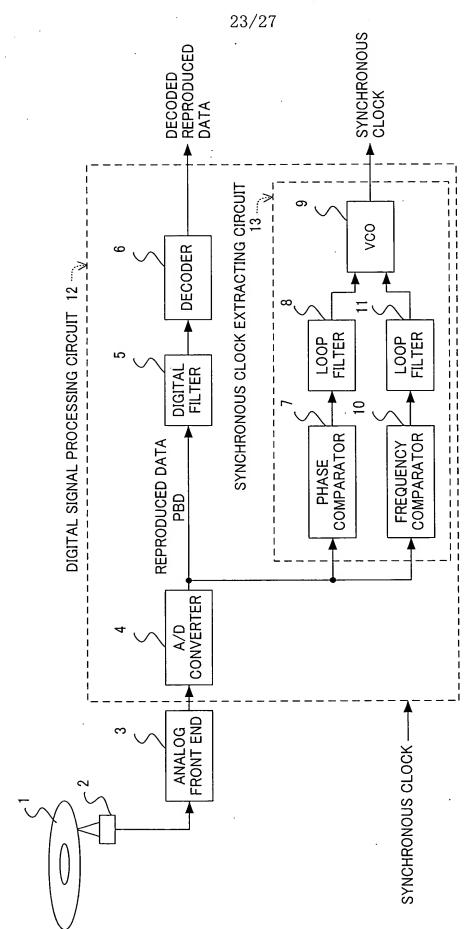


FIG. 23

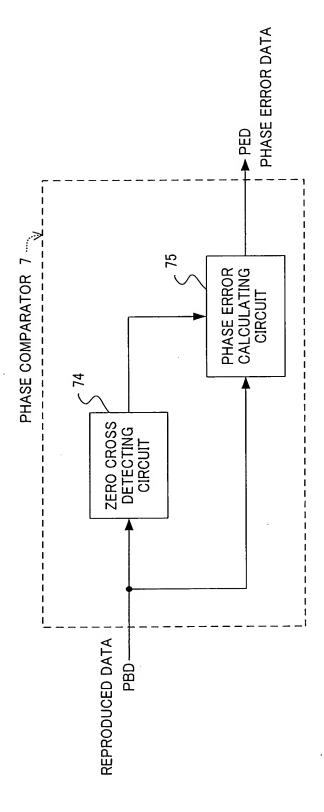
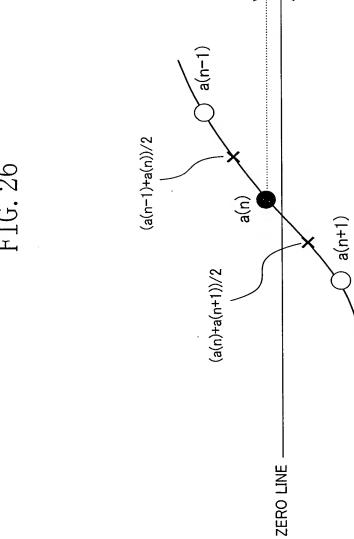


FIG. 24

FIG. 25

PHASE ERROR



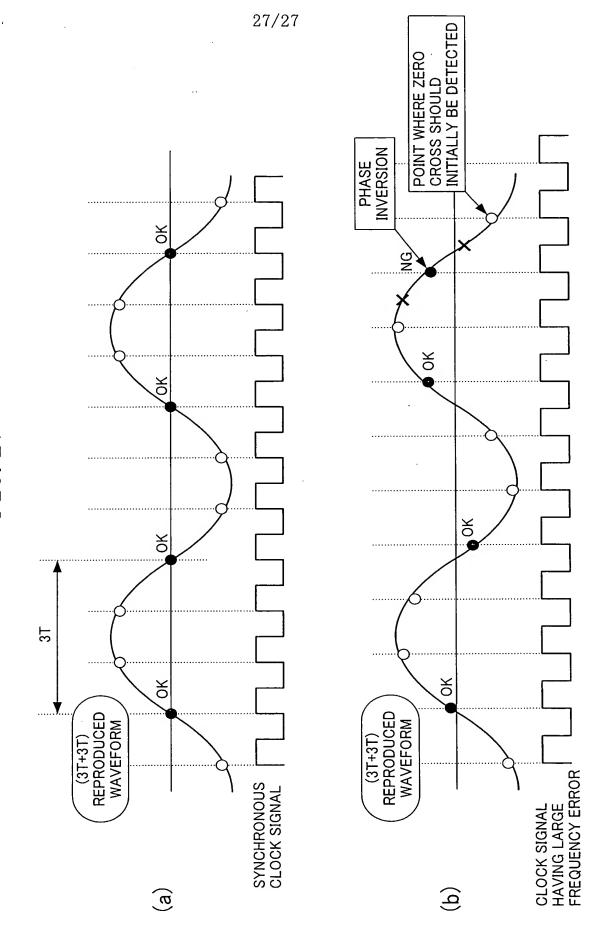


FIG. 27